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SLATER & MATSIL LLP

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Kristin R. Hayes (Depositor's Name)
KRHAYES (Signature)
February 6, 2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/635,621	08/06/2003	Hsi-Kuei Cheng	TSM02-0971	8481

TITLE OF INVENTION: METHOD FOR REDUCING DEFECTS IN POST PASSIVATION INTERCONNECT PROCESS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	02/17/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
NGUYEN, HA T	2812	438-597000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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(B) RESIDENCE (CITY AND STATE OR COUNTRY)

Taiwan Semiconductor Manufacturing Company, Ltd. Hsin-Chu, Taiwan

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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5. Change in Entry Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Typed or printed name Steven H. Slater

Date February 6, 2006

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FACSIMILE COVER SHEET

To: Commissioner for Patents	Total Pages Sent: 3
Mail Stop Issue Fee	(including cover sheet)
Facsimile Number: (571) 273-2885	Transmission Date: February 6, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Cheng, *et al.* Docket No.: TSM02-0971
Serial No: 10/635,621 Art Unit: 2812
Date Filed: August 6, 2003
Title: Method for Reducing Defects in Post Passivation Interconnect Process

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- Certification of Facsimile Transmission (1 page)
- Part B – Fee Transmittal (2 duplicate pages)

Respectfully submitted,

Kristin R. Hayes
Legal Assistant

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